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FN1040D002A

Product Specification Rev.V0

BUYER	
SUPPLIER	FANNAL Electronics CO., LTD
FG-Code	FN1040D002A

Preliminary	Specification
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☐ Approval Specification

ITEM	BUYER SIGNATURE DATE
	·

ITEM SUP	PLIER SIGN	ATURE DATE
Prepared	DONG	2022-11-28
Reviewed	XIONG	2022-11-28
Approved	JACK	2022-11-28

PRODUCT GROUP	REV	ISSUE DATE	
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REVISION HISTORY

REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
А		Initial Release	2022.11.28	JACK

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1.0 General Description /一般说明

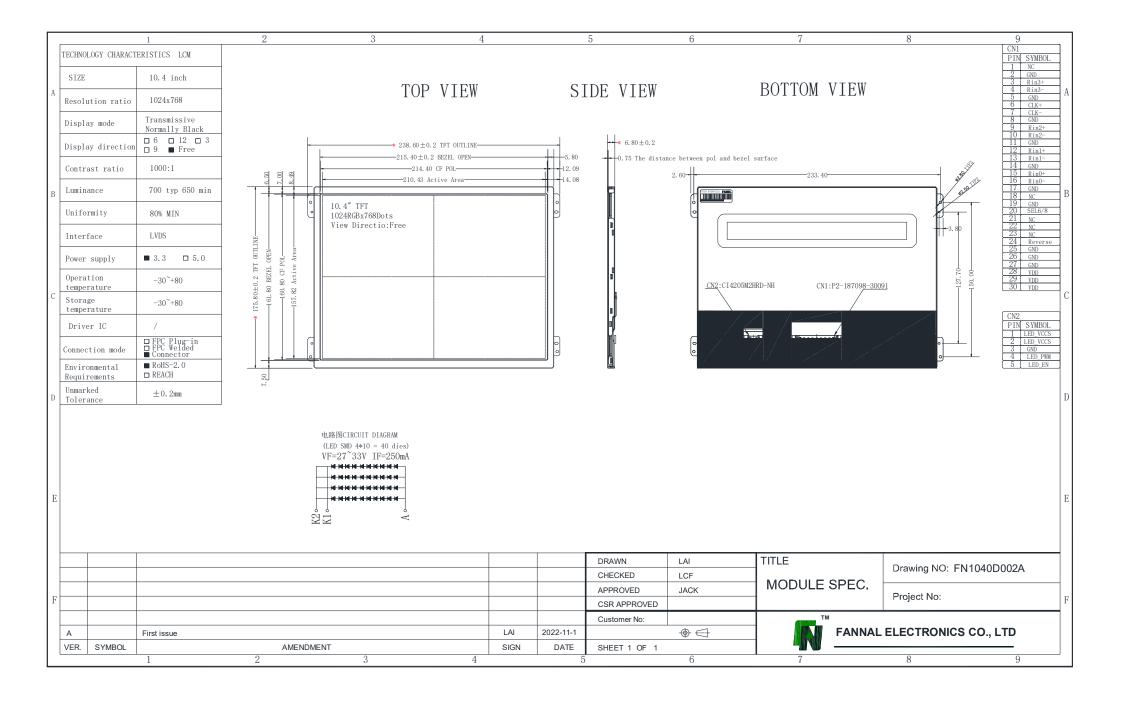
1.1 Application /应用

- Industrial
- Automotive
- Medical
- Outdoor highlight

1.2 General Specification /通用技术条件The followings are general specifications at the FN1040D002A

Parameter	Specification	Unit
LCD size	10.4 inch(Diagonal)	
Resolution	1024(H)RGB×768(V)	
Sub pixel size	0.2055(H)×0.2055(V)	mm
Active Area	210.432(H)×157.824(V)	mm
Display Mode	Normally Black, Transmissive	
View direction	ALL	
Module Size	238.6(W)×175.8(H)×6.8(D)	mm
Pixel driving element	a-Si TFT	
Interface	LVDS	
Driver IC	/	
Weight	305(Typ.)	g
Luminance	700(Typ.)	cd/m²

2.0 Mechanical Drawingr /机械制图



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3.0 ABSOLUTE MAXIMUM RATINGS /绝对最大额定值

 $[Ta = 25 \pm 2 \, ^{\circ}C]$

Parameter	Symbol	Min.	Max.	Unit
Dower Voltage	VDD	-0.3	3.8	V
Power Voltage	LED_VCCS	-0.3	25	V
Operating Temperature	T _{OP}	-30	80	°C
Storage Temperature	T _{ST}	-30	80	°C

Note:

If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

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4.0 ELECTRICAL SPECIFICATIONS/电气规范

4.1 TFT LCM Module

 $[Ta = 25 \pm 2 \, ^{\circ}C]$

Parameter	Symbol	Min.	Тур.	Max.	Unit
Dower Voltage	VDD	3.0	3.3	3.6	V
Power Voltage	LED_VCCS	11	12	13	V
Input logic high voltage	Vih	0.7VDD	-	VDD	V
Input logic low vogate	VIL	0	-	0.3VDD	V
Current for Dower	Ivdd		385	424	mA
Current for Power	ILED_VCCS	-	0.52	-	Α
LED EN Control Level	BL On	3.0	-	5.0	V
LED_EIV COINTOI ECVEI	BL Off	0	-	0.3	V
LED DWM Control Lovel	PWM High Level	3.0	-	5.0	V
LED_PWM Control Level	PWM Low Level	0	-	0.3	V
LED_PWM Control Frequency	f _{pwm}	1K	-	20K	Hz

^{1:} Including signal: SEL6/8 & Reverse

^{2:} LED_PWM duty >10%.

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4.2 LB Power output

 $[Ta = 25 \pm 2 \, ^{\circ}C]$

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Voltage for LED backlight	LED+	31	33	35	V	1
Current for LED backlgith	LED1- LED2-	78	80	82	mA	2

Note1: output power LED+ OVP is 40V. Note2: Set BL feedback 2 channels, each channel feedback current is 80mA.

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5.0 Interface Description/接口说明

Connector Name/Designation	Interface Connector/Interface Card		
Type Part Number	187098-30091(P-two)		
Mating Housing Part Number	187130-30xx(P-two)		

5.1 Pin assignment for LCM module /模组引脚分配

1 NC Reserved as BIST function/No connection 2 GND Ground 3 Rin3+ Positive LVDS differential data input 4 Rin3- Negative LVDS differential data input 5 GND Ground 6 CLK+ Positive Clock signal 7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode 21-23 NC No connection 24 Reverse Reverse panel function(Display rotation)	Pin No.	Symbol	Description
3 Rin3+ Positive LVDS differential data input 4 Rin3- Negative LVDS differential data input 5 GND Ground 6 CLK+ Positive Clock signal 7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	1	NC	Reserved as BIST function/No connection
4 Rin3- Negative LVDS differential data input 5 GND Ground 6 CLK+ Positive Clock signal 7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 LOW or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	2	GND	Ground
5 GND Ground 6 CLK+ Positive Clock signal 7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	3	Rin3+	Positive LVDS differential data input
6 CLK+ Positive Clock signal 7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	4	Rin3-	Negative LVDS differential data input
7 CLK- Negative Clock signal 8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	5	GND	Ground
8 GND Ground 9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	6	CLK+	Positive Clock signal
9 Rin2+ Positive LVDS differential data input 10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	7	CLK-	Negative Clock signal
10 Rin2- Negative LVDS differential data input 11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	8	GND	Ground
11 GND Ground 12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	9	Rin2+	Positive LVDS differential data input
12 Rin1+ Positive LVDS differential data input 13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	10	Rin2-	Negative LVDS differential data input
13 Rin1- Negative LVDS differential data input 14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	11	GND	Ground
14 GND Ground 15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	12	Rin1+	Positive LVDS differential data input
15 Rin0+ Positive LVDS differential data input 16 Rin0- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	13	Rin1-	Negative LVDS differential data input
16 Rino- Negative LVDS differential data input 17 GND Ground 18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	14	GND	Ground
17 GND Ground 18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	15	Rin0+	Positive LVDS differential data input
18 NC No connection 19 GND Ground Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	16	Rin0-	Negative LVDS differential data input
19 GND Ground Selection for 6 bits/8bits LVDS data input Low or NC: 8bit input mode High: 6bit input mode NC No connection	17	GND	Ground
Selection for 6 bits/8bits LVDS data input 20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	18	NC	No connection
20 SEL6/8 Low or NC: 8bit input mode High: 6bit input mode 21-23 NC No connection	19	GND	Ground
High: 6bit input mode 21-23 NC No connection			Selection for 6 bits/8bits LVDS data input
21-23 NC No connection	20	SEL6/8	Low or NC: 8bit input mode
			High: 6bit input mode
24 Reverse Reverse panel function(Display rotation)	21-23	NC	No connection
The residue parter fartetion (bisping) Totalion)	24	Reverse	Reverse panel function(Display rotation)
25-27 GND Ground	25-27	GND	Ground
28-30 VDD Power supply:+3.3V	28-30	VDD	Power supply:+3.3V

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Note:

- 1. Pin1 is reversed as BIST function for test, don't connect signal to this pin, keep floating.
- 2. SEL6/8 is used for selecting 6bit/8bit LVDS data input, L or NC: 8bit; High:6bit.
- 3. Pin21,22,23 are used as SPI interface for OTP function, don't connect any signal to thes e pin, and don't short them, keep floating.
- 4. Reverse pin is used for selecting scanning direction.

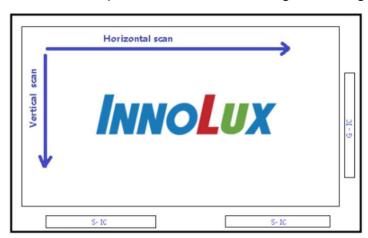




Fig. 1 Normal scan (Pin24, Reverse = Low or NC) Fig. 2 Reverse scan (Pin24, Reverse = High)

5.1 Connector 2:

Connector Name/Designation	Interface Connector/Interface Card	
Type Part Number	CI4205M2HRD-NH(CviLux)	
Mating Housing Part Number	CI4205SL000(CviLux)	

Pin No.	Symbol	Description
1	LED_VCCS	12V Input
2	LED_VCCS	12V Input
3	GND	Ground
4	LED_PWM	PWM
5	LED_EN	Converter power IC Enable(Active High)

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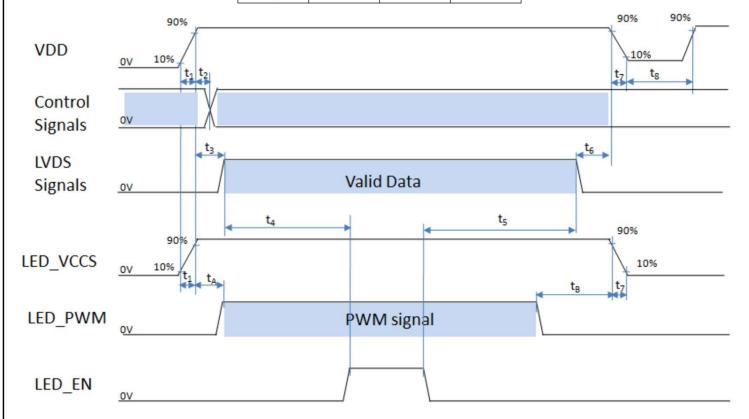


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5.2 Power Sequence/上电时序

The power sequence specifications are shown as the following table and diagram.

Cumbal	Va	Llmit		
Symbol	Min.	Max.	Unit	
t ₁	1	20	ms	
t ₂	1	5	ms	
t ₃	10	50	ms	
t ₄	200	500	ms	
t ₅	200	500	ms	
t ₆	50	200	ms	
t ₇	0	20	ms	
t ₈	500	-	ms	
t _A	0	50	ms	
t _B	0	50	ms	



- Note 1: Please don't plug the interface cable of on when system is turned on.
- Note 2: Please avoid floating state of the interface signal during signal invalid period.
- Note 3: It is recommended that the backlight power must be turned on after the power supply for LCD and the interface signal is valid.
- Note 4: Control signals include SEL6/8 & Reverse.

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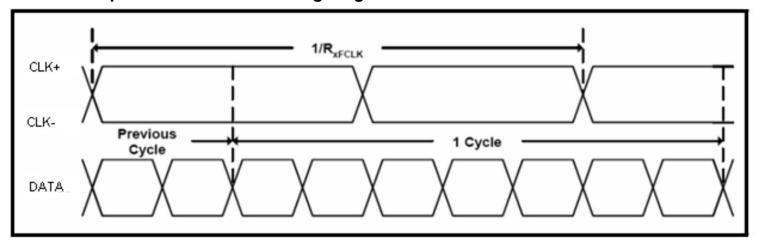
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5.3 LVDS Signal Timing Characteristics:

5.3.1 AC Electrical characteristics

Parameter	Symbol	Min	Тур	Max	Unit s	Condition
Clock frequency	RxFCLK	26.2	51.2	71	MHz	
Input data skew margin	TRSKM	500	500	1/(2*RxFCLK)	ps	Typical value for 1024*600 resolution
Clock high time	TLVCH		4/(7xRxFCLK)		ns	VID =400mv RxVCM=1.2V RxFCLK=71MHz VDD_LVDS=3.3V
Clock low time	TLVCL		3/(7xRxFCLK)		ns	
VSD setup time	TenPLL	0	TenPLL	150	us	

5.3.2 Input clock and data timing diagram

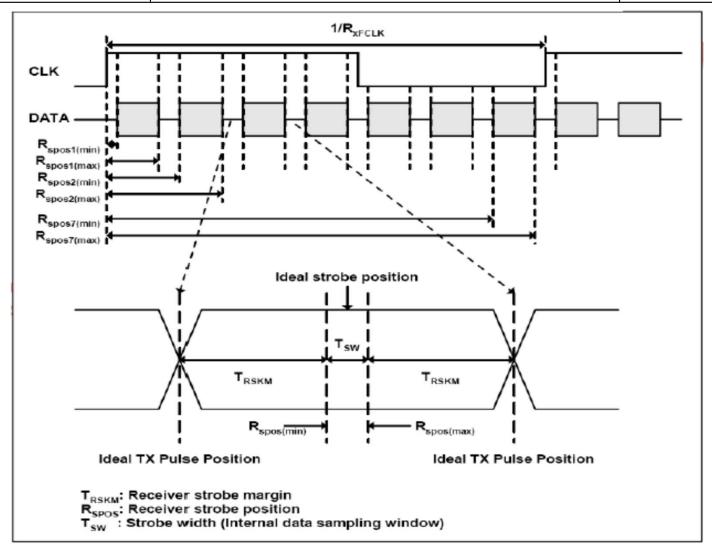


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5.4 DC electrical characteristics:

Davamatar	Cumbal	Values			I I m i f	Domonto
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
LVDS Differential input high	P		2	+100	m∨	
Threshold voltage	NXVTH	R _{XVTH} -	r.	+100	IIIV	R _{XVCM} =1.2V
LVDS Differential input low	D	-100			m∨	TXVCM-1.2V
Threshold voltage	R _{XVTL} -100	-	<u> </u>	ШУ		
Input ∀oltage range	р	0		VDD-1.2+	V	
(Singled-end)	R _{xVIN}	U		V _{ID} /2	V	
LVDS Differential input common	D	1\/1/2		VDD-1.2	V	
mode voltage	R _{xVCM}	V _{ID} /2		VUU-1.2	V	
LVDS Differential voltage	V _{ID}	0.2	4	0.6	V	

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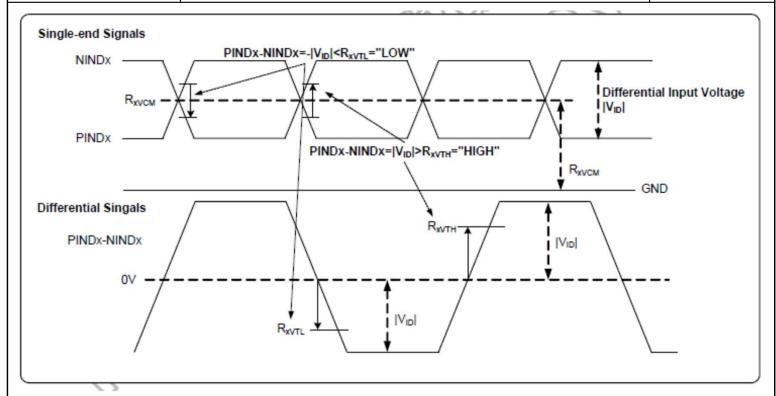
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5.5 Data timing:

Dominiotics.	Complete			1114	
Parameter	Symbol	Min.	Spec. Typ.	Max.	Unit
DCLK frequency	fclk	52	65	71	MHz
Horizontal display area	thd		DCLK		
HSD period	th	1114	1344	1400	DCLK
HSD blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd		768		T _H
VSD period	tv	778	806	845	T _H
VSD blanking	tvbp+tvfp	10	38 🛆	(//17)	T _H

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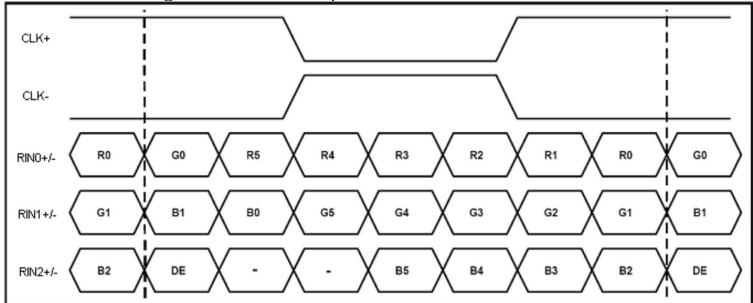


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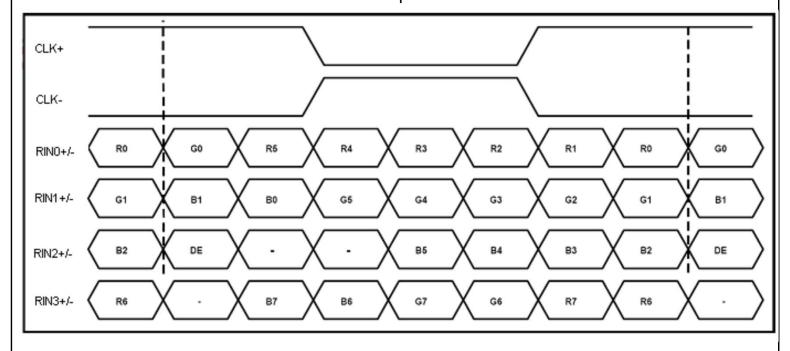
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5.6 LVDS data input format:

SEL6/8 = "High" for 6 bits LVDS Input



SEL6/8 = "Low" or "NC" for 8 bits LVDS Input



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6.0 OPTICAL SPECIFICATIONS /光学规格

6.1 Overview /概述

The test of optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2^{\circ}$ C) with the equipment of Luminance meter system (Goniom eter system and TOPCON BM-5) and test unit shall be located at an approximate distance 5 0cm from the LCD surface at a viewing angle of θ and Φ equal to Φ 0°. We refer to Φ 0=0 (= Φ 3) as the 3 o'clock direction (the "right"), Φ 0=90 (= Φ 12) as the 12 o'clock direction ("u pward"), Φ 0=180 (= Φ 9) as the 9 o'clock direction ("left") and Φ 0=270(= Φ 6) as the 6 o'clock direction ("bottom"). While scanning Φ and/or Φ 0, the center of the measuring spot on the display surface shall stay fixed.

6.2 Optical Specifications /光学规格

Item	Symbol	Condition	Min	Тур.	Max	Unit	Note
	θL		80	-	-		N . 4
Minusiana Amala	θ_{R}	Cr≥10	80	-	-	doa	
Viewing Angle	Ψτ	C1210	80	-	-	deg	Note 1
	Ψв		80	-	-		
Contrast Ratio	Cr	θ=0°	800	1000	-	ı	Note 2
Response Time	Tr+Tf	FF=0°	1	25	35	ms	Note 3
	Rx		0.270	0.300	0.330	4 3 9 4 4	
	Ry		0.304	0.334	0.364		
	Gx		0.593	0.623	0.653		
Color Coordinate	Gy	θ=0°	0.319	0.349	0.379		Note 4
of CIE1931	Bx	0=0	0.284	0.314	0.344		ivote 4
	Ву		0.574	0.604	0.634		
	Wx		0.124	0.154	0.184		
	Wy		0.038	0.068	0.098		
Uniformity	U		80			%	Note 5
Color Gamu	it		55	51.2		%	
Luminance	L		650	700	-	cd/m²	Note 6

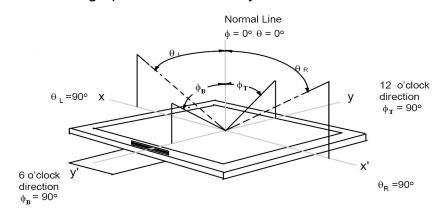
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Note 1:The definition of Viewing Angle

Refer to the graph below marked by θ and ϕ .



Note2:ThedefinitionofContrastRatio

(Contrast Ratio is measured in optimum common electrode voltage)

Note3:DefinitionofResponse time. (Test LCD using RD80S or similar equipments):

The output sign also photo detector are measured when the input sign also are changed from "black" to "white" (Voltage falling time) and from "white" to "black" (Voltage rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to fi gures below.

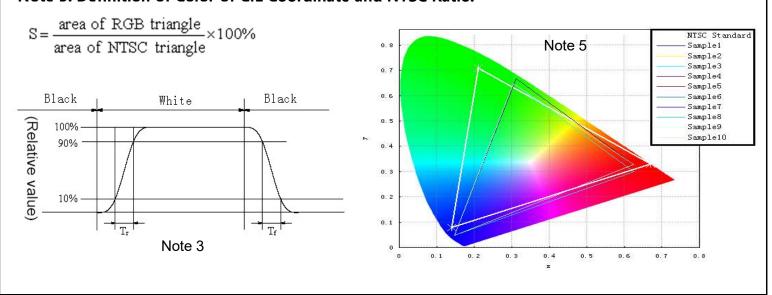
Note 4: Color Coordinates of CIE 1931

The test condition is at ILED=20mA and measured on the surface of LCD module at 25°C.

Measurement equipment:CS2000 or similar equipments

The Color Coordinate (CIE 1931) is the measurement of the center of the display shown in below figure.

Note 5: Definition of Color of CIE Coordinate and NTSC Ratio.



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7.0 RELIABLITY TEST /可靠性测试

The Reliability test items and its conditions are shown in below.

No	Test Items	Conditions	Testing standard
1	High temperature storage test	80°C 240hr	
2	Low temperature storage test	-30°C 240hr	IEC60068-2-1:2007
3	Low temperature operation test	-30°C 240hr	GB2423.2-2008
4	High temperature operation test	80°C 240hr	
5	High temperature & humidity (storage test)	60°C 90%RH 240hrs	IEC60068-2-78:2001 GB/T2423.3-2006
6	Thermal Shock Test	-30°C/0.5h ~ +80°C/0.5h for a total 100 cycles	Start with cold temp erature End with high tempe rature IEC60068-2-14:1984, GB2423.22-2002
7	Vibration Test	10Hz-55Hz 100m/s² 120min	
8	Mechanical shock	100G $\pm X$, $\pm Y$, $\pm Z$, 3times for each direction	
9	Dropping test	Drop in 1 corner,3 edges,6 surfaces, 1 time/direction Height follow ISTA(1A) 0kg\u220eW<10kg: 76cm, 10kg\u220eW<19kg: 61cm, 19kg\u220eW<28kg: 46cm, 28kg\u220eW<45kg: 31cm, 45kg\u220eW\u220e68kg: 20cm	IEC60068-2-32:1990 GB/T2423.8-1995
10	ESD test	C=150pF,R=330Ω, 5point/panel Air:±4Kv, 5times	IEC61000-4-2:2001 GB/T17626.2-2006 Class C

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· 8.0 Precautions /注意事项

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Please pay attention to the followings when you use this TFT LCD Panel.

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- · 8.1 Mounting Precautions /安装注意事项
- (1) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (2) You must mount a module using specified mounting holes (Details refer to the drawings).
- (3) Please make sure to avoid external forces applied to the Source PCB or FPC and D-IC during the process of handling or assembling. If not, It causes panel damage or malfunction.
- (4) Note that polarizers are very fragile and could be easily damaged. Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
- (5) Do not pull or fold the source D-IC which connect the source PCB or FPC and the panel.
- Do not pull or fold the LED wire.
- (6) After removing the protective film, when the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with alcohol or purified water.
- Do not strong polar solvent because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (9) Since the LCD is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it f alls from a high place or receives a strong shock, the glass may be broken.
- (10) Do not disassemble the module.
- (11) To determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- (12) If the customer's set presses the main parts of the LCD, the LCD may show the abnormal display. But this phenomenon does not mean the malfunction of the LCD and should be pressed by the way of mutual agreement.
- (13)Do not drop water or any chemicals onto the LCD's surface.

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8.2 Operating Precautions /操作注意事项

- (1) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (2) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (3) The electrochemical reaction caused by DC voltage will lead to LCD degradation, so DC drive should be avoided.
- (4) The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.
- (5) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- (6) Design the length of cable to connect between the connector for back-light and the converter as short as possible and the shorter cable shall be connected directly.
- The longer cable between that of back-light and that of converter may cause the luminance of LED to lower and need a higher startup voltage(Vs).
- (7) Connectors are precise devices for connecting PCB and transmitting electrical signals. Operators should insert and unplug MDL in parallel when assembling MDL.
- (8) Do not connect or disconnect the cable to/ from the module at the "Power On" condition.
- (9) When the module is operating, do not lose CLK, ENAB signals. If any one these signals is lost, the LCD panel would be damaged.
- (10) Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (11) Do not re-adjust variable resistor or switch etc.
- (12) For the Q/Single/OC Product, If the LED designed side view, LED bar should be putted in the L ong/short side; Otherwise, its reliability and function may not be guaranteed.

注:

- ①(1)涉及到Pol相关条目适用于OC/MDL出货产品,
- ②(6)(7)涉及到connector相关适用于OC/MDL出货产品
- ③ (12) 涉及到客户进行BLU设计, LED Bar位置需要避开GOA位置;

8.3 Electrostatic Discharge Control /静电放电控制

- (1) Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Keep products as far away from static electricity as possible.
- (2) Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

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8.4 Precautions for Strong Light Exposure /强光照射注意事项

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It is not allowed to store or run directly in strong light or in high temperature and humidity for a long ti me; Strong light exposure causes degradation of polarizer and color filter.

8.5 Storage Precautions /存储注意事项

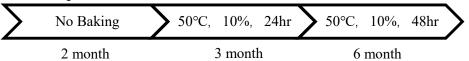
When storing modules as spares for a long time, the following precautions are necessary.

- •(1) The polarizer surface should not come in contact with any other object.
 - It is recommended that they be stored in the container in which they were shipped.

Temperature : $5 \sim 40 \, ^{\circ}\text{C}$

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- •(2) Humidity : 35 ~ 75 %RH
- •(3) Period: 6 months
- •(4) Control of ventilation and temperature is necessary.
- •(5) Please make sure to protect the product from strong light exposure, water or moisture. Be careful for condensation.
- •(6) Store in a polyethylene bag with sealed so as not to enter fresh air outside in it.
- •(7)Do not store the LCD near organic solvents or corrosive gasses.
- •(8) Please keep the Modules/OC/FOG at a circumstance shown below Fig.



8.6 Precautions for Protection Film /保护膜注意事项

- (1) Remove the protective film slowly, keeping the removing direction approximate
- 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- (2) In handling the LCD, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary.

8.7 Appropriate Condition for Display /适当的显示条件

- •(1) Normal operating condition
- Temperature: $0 \sim 40^{\circ}$ C
- Operating Ambient Humidity : $10 \sim 90 \%$
- Display pattern: dynamic pattern (Real display)
- Suitable operating time: under 12 hours a day.
- •(2) Special operating condition

If the product will be used in extreme conditions such as high temperature, humidity, display patterns or 7*24hrs operation time etc.., It is strongly recommended to contact us for Application engineering advice. Otherwise, its reliability and function may not be guaranteed.

•(3)Black image or moving image is strongly recommended as a screen save.

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- (4) Lifetime in this spec. is guaranteed only when commercial display is used according to operating usages.
- (5) Please contact us in advance when you display the same pattern for a long time.

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- (6) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" or "turn off" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (7) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module m ay be damaged.
- (8) Dew drop atmosphere should be avoided.

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- (9) The storage room should be equipped with a good ventilation facility and avoid to expose to corr osive gas, which has a temperature controlling system.
- (10) The LCD should be avoided to expose to corrosive gas for long time, ,the LCD may be affected by the gas as SO2 ,H2S etc.
- (11) When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD's surface which may affect the operation of the polarizer and the LCD.
- (12) Response time will be extremely delayed at lower temperature than the operating temperature r ange and on the other hand at higher temperature LCD may turn black at temperature above its opera tional range. However those phenomena do not mean malfunction or out of order with the LCD. The LCD will revert to normal operation once the temperature returns to the recommended temperature r ange for normal operation

8.8 Others /其他

A. LC Leak /液晶泄露

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.

B. Rework /返工

- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.
- C. In order to prevent potential problems, flicker should be adjusted by optimizing the Vcom value in customer LCM Line (适用于Q/Single/OC出货产品)

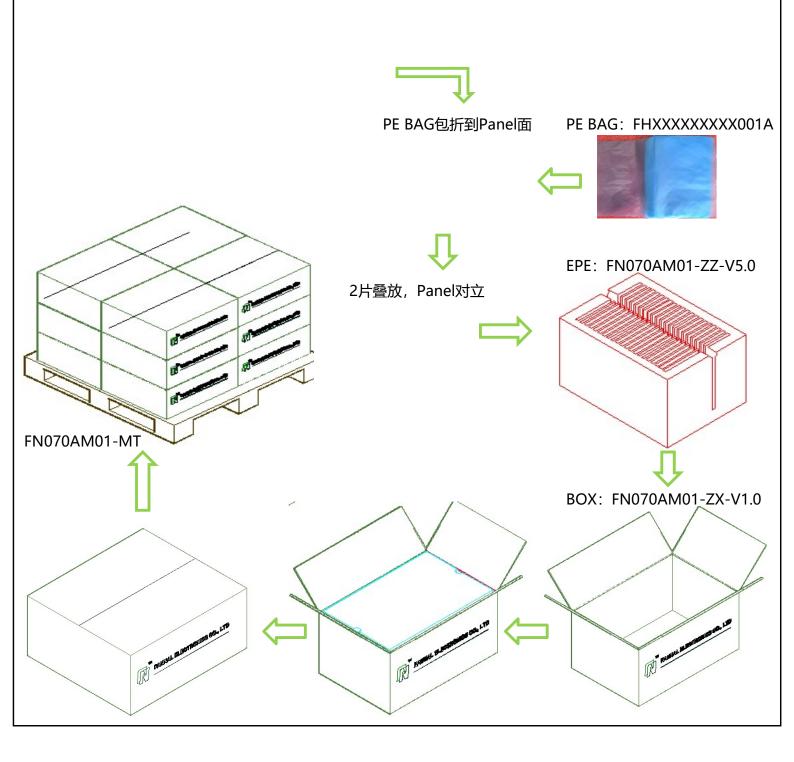
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9.0 PACKING INFORMATION(产品形态: LCM)

LCM MODEL	LCM Qty. in the Box	Carton Size(mm)	LCM Qty. in the Pallet
TBD	pcs/BOX	530*360*275mm	pcs/Pallet



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10.0 VISUAL INSPECTION CRITERIA FOR ALL CUSTMERS /所有客户的目视检查标准

10.1 Sampling Method /抽样方法

Unless otherwise agreed upon in writing, the sampling insepction shall be applied to the Customers incoming inspection.

10.1.1 Lot size: 1 pallet per same model

10.1.2 Sampling type: Random sampling

10.1.3 Inspection level: II

10.1.4 Sampling table : MIL-STD-105E

10.2 Inspection Environment /检验环境

10.2.1 Ambient conditions

a. Ambient Temperature:25±3°C

b. Relative Humidity:65±20%RH

c. Ambient Illumination:300-700LUX(Normal:500LUX)

10.2.2 Viewing Distance

The distance between the LCM and the inspector's eyes shall be at least 30cm-50cm

10.2.3 Viewing Angle

performing in front of the panel

[Vertical] : ± 25 degree [Horizontal] : ± 40 degree

10.2.4 Inspection Area:

Display Area(Active Area)

10.3 Definitions /定义

10.3.1 Dark / Bright Spots

Points on display which appear dark/bright and usually result form the contamination.

These defects do not vary in size or intensity(contrast)when contrast is varied.

10.3.2 Dark / Bright Lines

Lines on display which appear dark/bright and usually result from the contamination.

10.3.3 Polarizer Scratch

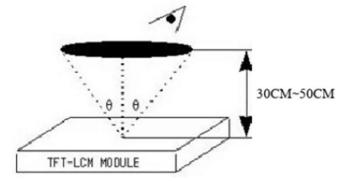
Lines on display which are seen across a darker background and do not vary in size.

10.3.4 Polarizer Dent

White spots on display which appear againse a darker backgound and do not vary in size.

103.5 Bright Dot Defects

Dots(sub-pixels)on display which appear bright in the display area and visible through the 5%ND filter at Black Pattern.



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10.3.6 Dark Dot Defects

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Dots(sub-pixels)on display which appear dark in the display area at R.G.B Color Patt ern.

10.3.7 Line Defects

All line defects on display which appear brigh/dark such as vertical, horizontal, or cross lines.

10.3.8 Mura

Mura on display which appears darker/brighter against background birghtness on part s of display area.

10.3.9 BM Defects

Bright(white)Points on display which are off BM(Black Matrix).

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10.3.10 Visual Inspection

Inspection for LCM when the unit turns on.

10.3.11 Appearance Inspection

External inspection for LCM when the unit turns off.

10.3.12 Other

Defects which cannot be classified into the above defect definitions.

Note 1: Bright& Dark dots are not smaller than a sub-pixel (Dots smaller than a sub-pixel are not counted as d efect dots)

10.4 Inspectin Criteria /检验标准

Refer to 《TFT LCM general inspection standard》

10.5 Verification /验证

The supplier can verify the defective LCMs to segregate the responsibilities at customer's facility or can request the Customer to ship the defective LCMs to assigned place for verification

This verification result shall be agreed mutually buy the Customer and Supplier. This result can be corrected/changed after detail failure analysis at Supplier's facilities.

10.6 Supplier Induced Defects /供应商引起的缺陷

All of the Supplier induced defective LCMs shall be returned to the Supplier for repair or replacement.

Bfore return the defective LCMs, the Customer needs Supplier's confirmatin with RMA Number.

All of the returned LCMs shall be returned to the Customer within agreed time period.

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10.7 Customer Induced Defects /顾客引起的缺陷

The Customer can return the custmoer induced defective LCMs to the Supplier for repair. The repair cost for Customer induced defective LCMs shall be agreed with both parties, Customer and Supplier.

10.8 Warranty Period /质量保证期

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In-warranty period is Eighteen(18)Months from manufacturing month of LCM Note:

- a. Eighteen months are composed of twelfth months in-warranty period and sixth mon ths distribution period
 - b. The manufacturing Month is on the LCMs as Supplier's serial No.

10.9 Repair Warranty /维修保证书

Repair warranty is Twelve(12)Months from repaired month for repaired LCMs Note: a. The Label for repair will be added after repairing.

10.10 Warranty avoidance /避免担保

The warranty will be avoided in cases of below:

- a. When the warranty period is expired.
- b. The Customer induced defective LCMs.
- c. When the LCMs were repaired by 3rd party without Suppolier's approval.
- d. When the LCMs were treated like Disassemble and Rework by the Customer and/or Customer's representatives without Supplier's approval.

10.11 Others /其他

If any problems arise with the LCMs supplied by supplier, the customer and supplier will coopeate and make ettorts to solve it with mutual contidence and respect