



GENERAL INFORMATION		
Item	Contents	Unit
LCD Size	7.0	Inch
Number Of Pixels	1024(H)RGB×600(V)	---
Sub Pixel Pitch	0.0502(H) × 0.1432(V)	mm
Active Area	154.2144(H) × 85.92(V)	mm
Viewing area (W x H)	157.0 x 89.5	mm
LCM outline (W x H x T)	165.03(W) × 100.03(H) × 5.65(D)	mm
Display mode	IPS,NB	---
View angle (L/R/U/D)	85/85/85/85	Degree
TFT Driver IC	HX8282+HX8696	---
Interface Type	LVDS	---
Color depth	16.7M	color
LCM brightness	TYP 600	Cd/m²
With/Without tp	/	---
TFT Power consumption	70	mw
BL Power consumption	1800	mw
Operation Temperature	-20~70	°C
LED life time	30,000	Hrs

ELECTRICAL CHARACTERISTICS						INTERFACE DESCRIPTION			
Parameter	Symbol	Min.	Typ.	Max.	Unit	No.	Symbol	I/O	Description
Power Supply Voltage	VDD	2.3	3.3	3.6	V	1	VCOM	P	Common Voltage
	AVDD	-	9.6	-	V	2	VDD	P	Power Voltage for digital circuit
	VGH	-	18.0	-	V	3	VDD	P	Power Voltage for digital circuit
	VGL	-	-6	-	V	4	NC	-	No connection
	VCOM	3.1	3.2	3.3	V	5	RESET	I	Global reset pin
Power Supply current	IDD	-	21	-	mA	6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
Input logic high voltage	VIH	0.7VDD	-	VDD	V	7	GND	P	Ground
Input logic low voltage	VIL	0	-	0.3VDD	V	8	RXINO-	I	-LVDS differential data input
BACKLIGHT DRIVING CONDITION						9	RXINO+	I	+LVDS differential data input
Item	Symbol	Min	Typ.	Max	Unit	10	GND	P	Ground
Forward current	If	--	200	--	mA	11	RXIN1-	I	-LVDS differential data input
Forward voltage	Vf	8.4	9.0	10.5	V	12	RXIN1+	I	+LVDS differential data input
						13	GND	P	Ground
						14	RXIN2-	I	-LVDS differential data input
						15	RXIN2+	I	+LVDS differential data input
						16	GND	P	Ground
						17	RXCLKIN-	I	-LVDS differential data input
						18	RXCLKIN+	I	+LVDS differential data input
						19	GND	P	Ground
						20	RXIN3-	I	-LVDS differential data input
						21	RXIN3+	I	+LVDS differential data input
						22	GND	P	Ground
						23	NC	-	No connection
						24	NC	-	No connection
						25	GND	P	Ground
						26	NC	-	No connection
						27	DIMO	I	Backlight CABC controller signal output
						28	SELB	I	6bit/8bit mode select. SELB=0, LVDS 8 BIT; SELB=1, LVDS 6BIT
						29	AVDD	P	Power for Analog Circuit
						30	GND	P	Ground.
						31-32	LEDK-	P	LED Cathode
						33	SHLR	I	Horizontal inversion
						34	UPDN	I	Vertical inversion
						35	VGL	P	Gate OFF Voltage
						36	NC	-	No connection
						37	NC	-	No connection
						38	VGH	P	Gate ON Voltage
						39-40	LEDA+	P	LED Anode