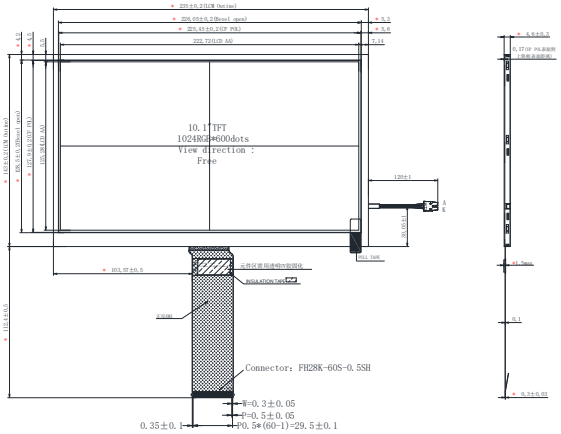
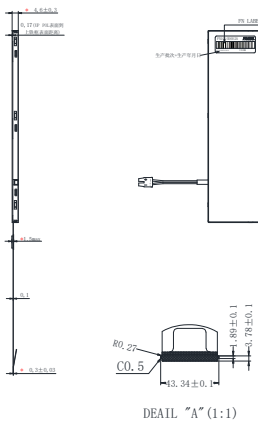
FANNAL ELECTRONICS

PRODUCT GROUP
TFT- LCM

FN1010D002A





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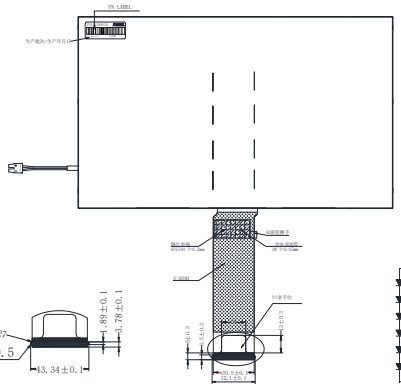

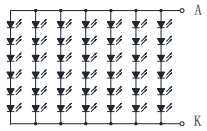

电路图CIRCUIT DIAGRAM
(LED SMD 6*7LED = 42 LED)
IF=200mA VF=16.2~20.4V

GENERAL INFORMATION		
Item	Contents	Unit
LCD Size	10.1	Inch
Number Of Pixels	1024(H)RGB×600(V)	---
Pixel pitch (W x H)	0.2088×0.2175	mm
Active Area	222.72 (H) x 125.28 (V)	mm
Viewing area (W x H)	226.05×127.9	mm
LCM outline (W x H x T)	235(W)×143.0(H)×4.6(D)	mm
Display mode	NB	---
View angle (L/R/U/D)	80/80/80/80	Degree
TFT Driver IC	ILI6150&ILI5120	---
Interface Type	LVDS	---
Color depth	16.7M	Color
LCM brightness	800(TYP)	Cd/m²
With/Without tp	/	---
TFT Power consumption	49.5	mw
BL Power consumption	3600	mw
Operation Temperature	-30~85	℃
LED life time	30,000	Hrs

ELECTRICAL CHARACTERISTICS					
Parameter	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	VDD	3.0	3.3	3.6	V
	AVDD	10	12.5	14	V
	VGH	19	20	25	V
	VGL	-13	-10	-7	V
	VCOM	4.39	5.39	6.39	V
Input logic high voltage	VIH	0.7VDD	-	VDD	V
Input logic low voltage	VIL	0	-	0.3VDD	V

BACKLIGHT DRIVING CONDITION					
Item	Symbol	Min	Typ.	Max	Unit
Forward current	If	--	200	--	mA
Forward voltage	Vf	16.2	18.0	20.4	V

INTERFACE DESCRIPTION			
No.	SYMBOL	I/O	Description
1	GND	P	Ground
2	AVDD	P	Power for analog circuit
3	VDD	P	Power supply VDD=3.3V TYP
4	GND	P	Ground
5	VCOM	I	Power supply for the TFT-LCD common electrode
6	VDD	P	Power supply VDD=3.3V TYP
7	GND	P	Ground
8-14	NC	-	No Connection
15	GND	P	Ground
16	VDD	P	Power supply VDD=3.3V TYP
17	GND	P	Ground
18	RXIN3+	I	LVDS Positive data signal(+)
19	RXIN3-	I	LVDS Negative data signal(-)
20	GND	P	Ground
21	RXCLK+	I	LVDS Positive CLK signal(+)
22	RXCLK-	I	LVDS Negative CLK signal(-)
23	GND	P	Ground
24	RXIN2+	I	LVDS Positive data signal(+)
25	RXIN2-	I	LVDS Negative data signal(-)
26	GND	P	Ground
27	RXIN1+	I	LVDS Positive data signal(+)
28	RXIN1-	I	LVDS Negative data signal(-)
29	GND	P	Ground
30	RXIN0+	I	LVDS Positive data signal(+)
31	RXIN0-	I	LVDS Negative data signal(-)
32-33	GND	P	Ground
34	RESET	I	Global reset pin
35	STBYB	I	Standby mode . Normally pulled high STBYB = "1", normal operation ; STBYB = "0", timing controller, source driver will turn off, all output are High-Z
36	SHLR	I	Left / right selection. SHLR = "L", shift left: last data = SO1←SO2←SO3.....←SO1536 = first data. SHLR = "H", shift right: first data = SO1→SO2→SO3.....→SO1536 = last data
37	VDD	P	Power supply VDD=3.3V TYP
38	UPDN	I	Up/down selection. UPDN = "L", STV2 output vertical start pulse and UD pin output logical "0" to Gate driver. UPDN = "H", STV1 output vertical start pulse and UD pin output logical "1" to Gate driver.
39	GND	P	Ground
40	AVDD	P	Power for analog circuit
41	VCOM	P	Power supply for the TFT-LCD common electrode
42	DITH	I	Dithering function . DITHER = "1", Enable internal dithering function DITHER = "0", Disable internal dithering function
43	GND	P	Ground
44	VDD	P	Power supply VDD=3.3V TYP
45	GND	P	Ground
46-52	NC	-	No Connection
53	GND	P	Ground
54	VDD	P	Power supply VDD=3.3V TYP
55	SELB	I	SELB=0, LVDS 8 BIT ; SELB=1, LVDS 6BIT
56	VGH	I	Gate on voltage
57	VDD	P	Power supply VDD=3.3V TYP
58	VGL	I	Gate off voltage
59	GND	P	Ground
60	BIST	I	Normal Operation/BIST pattern select. Normally pull low